# Parallel generation of $\ell$-sequences 

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- Synthesis of sub-sequences
- Multiple steps LFSR
- Parallel generation of $\ell$-sequences (FCSRs)
- Synthesis of sub-sequences
- Multiple steps FCSR
- Conclusion


## Part 1 <br> Introduction

## Sub-sequences generator



- Goal: parallelism
- better throughput
- reduced power consumption


## Notations

- $S=\left(s_{0}, s_{1}, s_{2}, \cdots\right)$ : Binary sequence with period $T$.
- $S_{d}^{i}=\left(s_{i}, s_{i+d}, s_{i+2 d}, \cdots\right)$ : Decimated sequence, with $0 \leq i \leq d-1$.
- $S_{d}^{0}=\left(s_{0}, s_{d}, \cdots\right), \cdots, S_{d}^{d-1}=\left(s_{d-1}, s_{2 d-1}, \cdots\right)$
- $x_{j}$ : Memory cell. \|
- $\left(x_{j}\right)_{t}$ : Content of the cell $x_{j}$.
- $X_{t}$ : Entire internal state of the automaton. II
- $n e x t^{d}\left(x_{j}\right)$ : Cell connected to the output of $x_{j}$. I


## LFSRs

- Automaton with linear update function. \|
- Let $s(x)=\sum_{i=0}^{\infty} s_{i} x^{i}$ be the power series of $S=\left(s_{0}, s_{1}, s_{2}, \ldots\right)$. There exists two polynomials $p(x), q(x)$ :

$$
s(x)=\frac{p(x)}{q(x)}
$$

- $q(x)$ : Connection polynomial of degree $m$.
- $Q(x)=x^{m} q(1 / x)$ : Characteristic polynomial.
- m-sequence: $S$ has maximal period of $2^{m}-1$. (iff $q(x)$ is a primitive polynomial)
- Linear complexity: Size of smallest LFSR which generates $S$.

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## Fibonacci/Galois LFSRs

Fibonacci setup.


## FCSRs

[Klapper Goresky 93]

- Instead of XOR, FCSRs use additions with carry.II
- Non-linear update function.
- Additional memory to store the carry.
- $S$ is the 2-adic expansion of the rational number: $\frac{h}{q} \leq 0$. \|
- Connection integer $q$ : Determines the feedback positions.
- $\ell$-sequences: $S$ has maximal period $\varphi(q)$.
(iff $q$ is odd and a prime power and $\operatorname{ord}_{q}(2)=\varphi(q)$.) \|
- 2-adic complexity: size of the smallest FCSR which produces $S$.

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## Fibonacci/Galois FCSRs [Klapper Goresky 02]

Fibonacci setup.


Galois setup.


## Part 2

Parallel generation of $m$-sequences
(LFSRs)

## Synthesis of Sub-sequences (1)



- Use Berlekamp-Massey algorithm to find the smallest LFSR for each sub-sequence. II
- All sub-sequences are generated using $d$ LFSRs defined by $Q^{\star}(x)$ but initialized with different values. II

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## Synthesis of Sub-sequences (2)

Theorem [Zierler 59]: Let $S$ be produced by an LFSR whose characteristic polynomial $Q(x)$ is irreducible in $\mathbf{F}_{2}$ of degree $m$. Let $\alpha$ be a root of $Q(x)$ and let $T$ be the period of $S$. For $0 \leq i<d, S_{d}^{i}$ can be generated by an LFSR with the following properties:

- The minimum polynomial of $\alpha^{d}$ in $\mathbf{F}_{2^{m}}$ is the characteristic polynomial $Q^{\star}(x)$ of the new LFSR with:
- Period $T^{\star}=\frac{T}{\operatorname{gcd}(d, T)}$.
- Degree $m^{\star}$ is the multiplicative order of 2 in $\mathbf{Z}_{T^{\star}}$.


## Multiple steps LFSR

[Lempel Eastman 71]

- Clock $d$ times the register in one cycle. II
- Equivalent to partition the register into $d$ sub-registers

$$
x_{i} x_{i+d} \cdots x_{i+k d}
$$

such that $0 \leq i<d$ and $i+k d<m$.

- Duplication of the feedback:

The sub-registers are linearly interconnected.

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## Fibonacci LFSR

$$
\begin{aligned}
& \operatorname{next}^{1}\left(x_{0}\right)=x_{3} \\
& \operatorname{next}^{1}\left(x_{i}\right)=x_{i-1} \text { if } i \neq 0 \\
& \left(x_{3}\right)_{t+1}=\left(x_{3}\right)_{t} \oplus\left(x_{0}\right)_{t} \\
& \left(x_{i}\right)_{t+1}=\left(x_{i-1}\right)_{t} \text { if } i \neq 3
\end{aligned}
$$

$n e x t t^{2}\left(x_{0}\right)=x_{2}$
$n \operatorname{ext}^{2}\left(x_{1}\right)=x_{3}$
$n e x t^{2}\left(x_{i}\right)=x_{i-2}$ if $i>1$
$\left(x_{i}\right)_{t+2}=\left(x_{i-2}\right)_{t}$ if $i<2$
$\left(x_{2}\right)_{t+2}=\left(x_{3}\right)_{t} \oplus\left(x_{0}\right)_{t}$
$\left(x_{3}\right)_{t+2}=\underbrace{\left(x_{3}\right)_{t} \oplus\left(x_{0}\right)_{t}}_{\left(x_{3}\right)_{t+1}} \oplus\left(x_{1}\right)_{t}$

## 2-decimation

$\rightarrow f\left(X_{t}\right)$
$\rightarrow f\left(X_{t+1}\right)$


## Comparison

- Synthesis of Sub-sequences:
- Larger memory size: $d \times m^{\star}$
- More logic gates: $d \times w t\left(Q^{\star}\right)$
- Multiple steps LFSR:
- Same memory size: $m$
- More logic gates: $d \times w t(Q)$


## Part 3

Parallel generation of $\ell$-sequences (FCSRs)

## Synthesis of Sub-sequences (1)



- We use an algorithm based on Euclid's algorithm [Arnault Berger Necer 04] or on lattice approximation [Klapper Goresky 97] to find the smallest FCSR for each subsequence.
- The sub-sequences do not have the same $q$.


## Synthesis of Sub-sequences (2)

- A given $S_{d}^{i}$ has period $T^{\star}$ and minimal connection integer $q^{\star}$.
- Period: (True for all periodic sequences)
- $T^{\star} \left\lvert\, \frac{T}{\operatorname{gcd}(T, d)}\right.$,
- If $\operatorname{gcd}(T, d)=1$ then $T^{\star}=T$.
- If $\operatorname{gcd}(T, d)>1: T^{\star}$ might depend on $i$ !
E.g. for $S=-1 / 19$ and $d=3: T / \operatorname{gcd}(T, d)=6$.
- $S_{3}^{0}$ : The period $T^{\star}=2$.
- $S_{3}^{1}$ : The period $T^{\star}=6$.
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## Synthesis of Sub-sequences (3)

- 2-adic complexity [Goresky Klapper 97]:
- General case: $q^{\star} \mid 2^{T^{\star}}-1 . \|$
- $\operatorname{gcd}(T, d)=1: q^{\star} \mid 2^{T / 2}+1$.
- Conjecture [Goresky Klapper 97]: Let $S$ be an $\ell$-sequence with connection integer $q=p^{e}$ and period $T$. Suppose $p$ is prime and $q \notin\{5,9,11,13\}$. For any $d_{1}, d_{2}$ relatively prime to $T$ and incongruent modulo $T$ and any $i, j$ :

$$
S_{d_{1}}^{i} \text { and } S_{d_{2}}^{j} \text { are cyclically distinct. }
$$

- Based on Conjecture:
- If $q$ is prime and $\operatorname{gcd}(T, d)=1$ then $q^{\star}>q$.
- Let $q, p$ be prime and $T=q-1=2 p$ :

$$
1 \leq d<T, \text { and } d \neq p \text { then } q^{\star}>q .
$$

## Multiple steps FCSR

- Clock $d$ times the register in one cycle. \|
- Equivalent to partition the register into $d$ sub-registers

$$
x_{i} x_{i+d} \cdots x_{i+k d}
$$

such that $0 \leq i<d$ and $i+k d<m$. \|

- Interconnection of the sub-registers.
- Propagation of the carry computation. I

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## Fibonacci FCSR

1-decimation


## Galois FCSR

1-decimation


$$
\begin{aligned}
& A=\boxplus\left[\left(x_{0}\right)_{t},\left(x_{1}\right)_{t},\left(c_{0}\right)_{t}\right] \bmod 2 \\
& B=\boxplus\left[\left(x_{0}\right)_{t},\left(x_{1}\right)_{t},\left(c_{0}\right)_{t}\right] \div 2 \\
& \left(x_{0}\right)_{t+2}=\boxplus\left[A, B,\left(x_{2}\right)_{t}\right] \bmod 2 \\
& \left(c_{0}\right)_{t+2}=\boxplus\left[A, B,\left(x_{2}\right)_{t}\right]_{\div 2} \\
& \left(x_{1}\right)_{t+2}=\left(x_{3}\right)_{t} \\
& \left(x_{2}\right)_{t+2}=\left(x_{0}\right)_{t} \\
& \left(x_{3}\right)_{t+2}=A
\end{aligned}
$$

## Carry Propagation

- Efficient implementation by means of $n$-bit ripple carry adder:

2-bit ripple carry adder


## Comparison

- Synthesis of Sub-sequences:
- Period: If $\operatorname{gcd}(T, d)>1$ it might depend on $i$.
- 2-adic complexity: $q^{\star}$ can be much bigger than $q$.
- Multiple steps FCSR:
- Same memory size.
- Propagation of carry by well-known arithmetic circuits.


## Part 4 <br> Conclusion

## Conclusion

- The decimation of an $\ell$-sequence can be used to increase the throughput or to reduce the power consumption.
- A separated FCSR for each sub-sequence is not satisfying. However, the multiple steps FCSR works fine (even with carry).
- Efficient software implementation: 14-bit FCSR with $q=18433$.

| Implementation | Throughput |
| :---: | :---: |
| classic | $2.7 \mathrm{MByte} / \mathrm{s}$ |
| decimated $(d=8)$ | $19 \mathrm{MByte} / \mathrm{s}$ |

- Future Work: How to find the best $q$ for hardware/software implementation?

Watermill generator

