



1. Give the reachability graph for the P/T net above.
2. List all the deadlocks in the reachability graph.
3. Generate a Promela program simulating the net using the translation given in the Lectures.
4. Generate a synchronization of LTSs with the same reachability graph as the net above, alphabet $\Sigma = \{t_1, t_2, t_3, t_4\}$ and two state LTSs L_1, L_2, L_3 , and L_4 representing the contents of each one the places p_1, p_2, p_3 , and p_4 .